

E6		G5										
E5	USB_D-	NC_G5	B6									
D4	USB_D+	NC_B6	D7									
A4	USB_VBUS	NC_D7	E7									
A5		NC_E7	F7									
A1	C103/TXD	NC_F7	G7									
B1	C104/RXD	NC_G7	B8									
A3	C105/RTS	NC_B8	C8									
A2	C106/CTS	RFU_C8	A9									
B2	C107/DSR	NC_A9	B9									
B3	C108/DTR	NC_B9	C9									
C1	C109/DCD	NC_C9	D9									
C2	C125/RING	NC_D9	E9									
C7	TXD_AUX/SPI_MOSI	NC_E9	F9									
B7	RXD_AUX/SPI_MISO	NC_F9	G9									
A7	SIMVCC	NC_G9	A10									
A6	SIMRST	NC_A10	B10									
	SIMCLK	NC_B10	C10									
	SIMIO	NC_C10	D10									
C5	GPIO_01/DVI_WA0	NC_D10	E10									
C6	GPIO_02/JDR/DVI_RX	RFU_E10	F10									
D6	GPIO_03/DVI_TX	NC_F10	G10									
D5	GPIO_04/DVI_CLK	NC_G10	B11									
B5	GPIO_05	NC_B11	C11									
B4	GPIO_06	NC_C11	F5									
C4	GPIO_07											
D3	RFU_D3											
E8	RFU_E8											
F8	RFU_F8											
D8	RFU_D8											
F4	ADC_IN1											
E4	DAC_OUT											
G2	ANTENNA											
G4	RESET*											
G6	VAUX/PWRMON											
F11	SPI_CLK											
E11	SPI_SRDY											
D11	SPI_MRDY											

UE866

<Value>

U?

VBATT

VBATT_PA

E2

E1

D1	GND_D1
F1	GND_F1
G1	GND_G1
D2	GND_D2
F2	GND_F2
C3	GND_C3
E3	GND_E3
F3	GND_F3
G3	GND_G3
F6	GND_F6
A8	GND_A8
G8	GND_G8
A11	GND_A11
G11	GND_G11